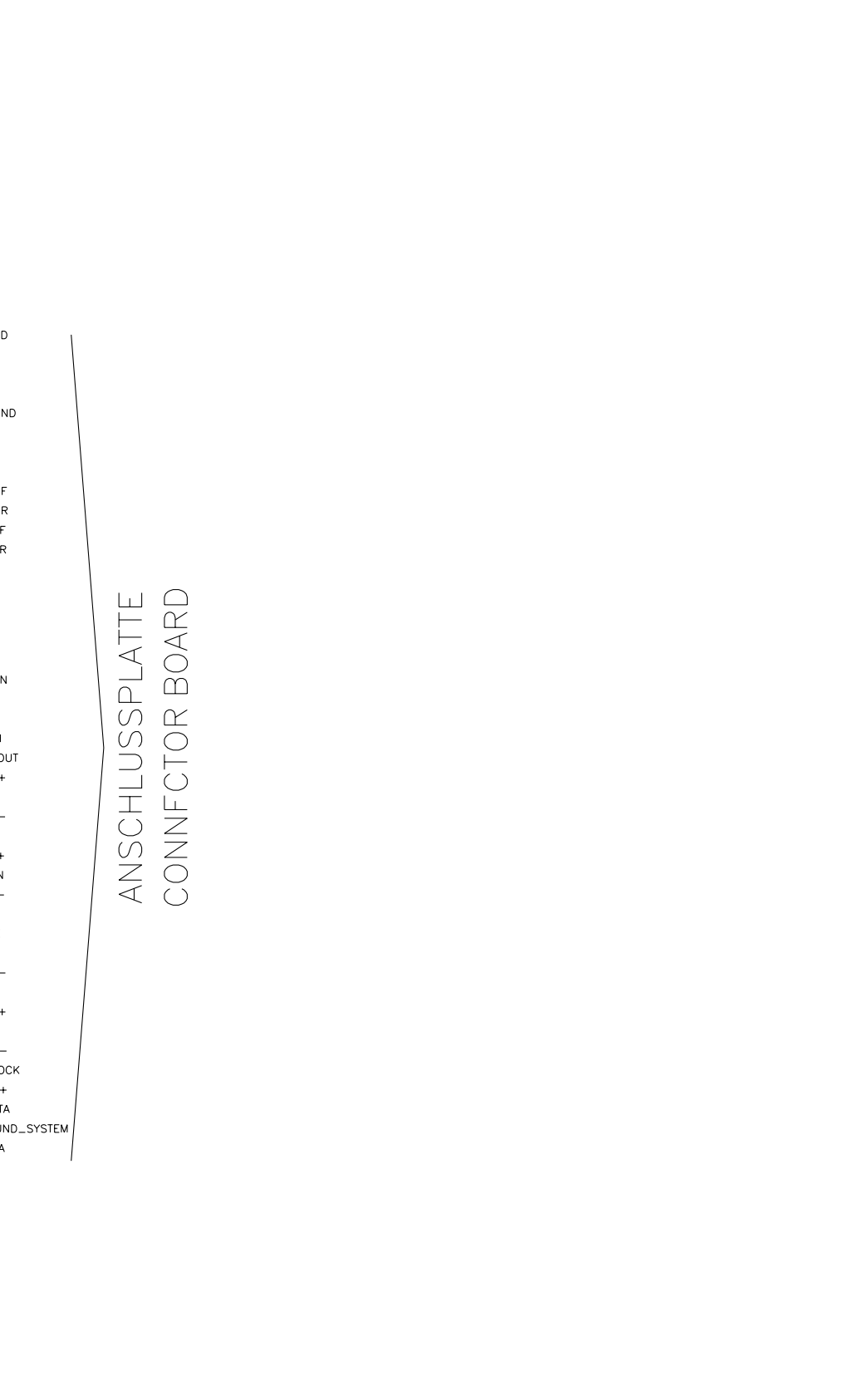
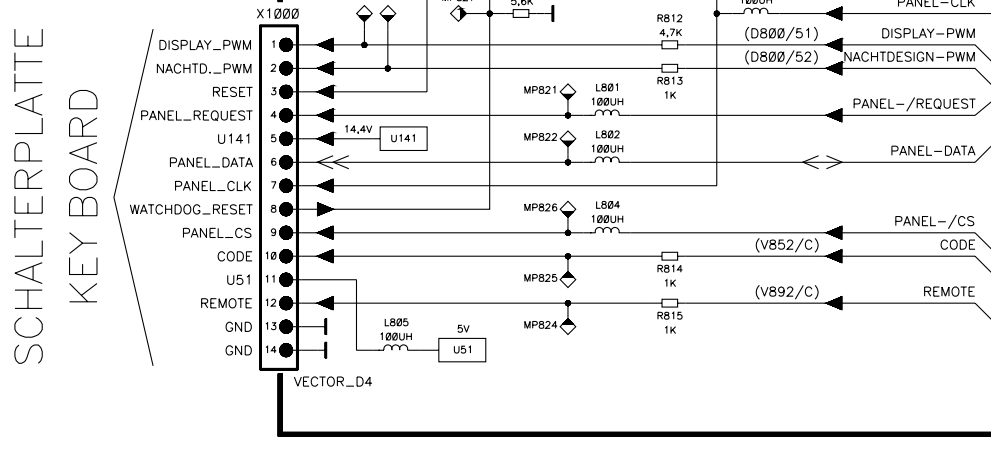
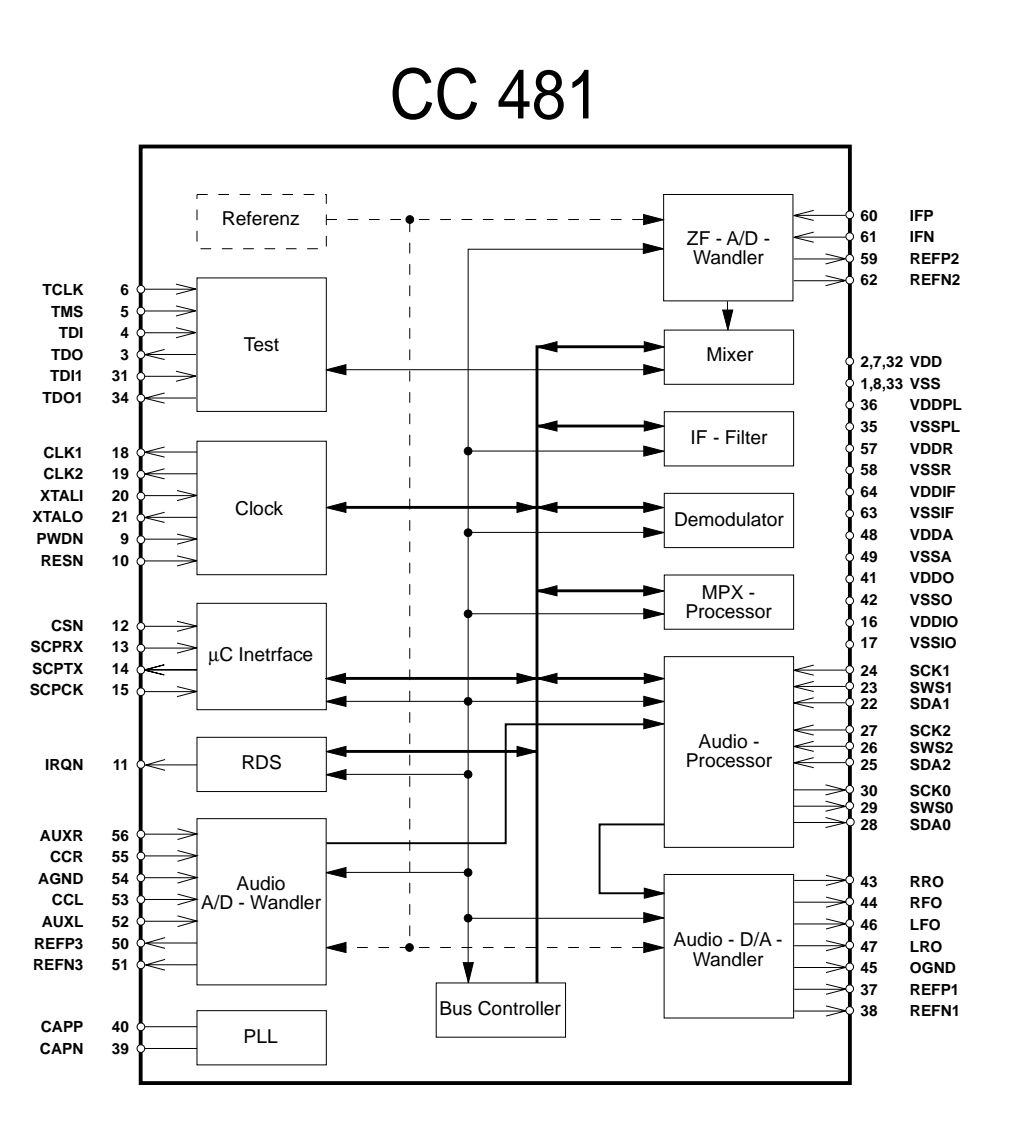
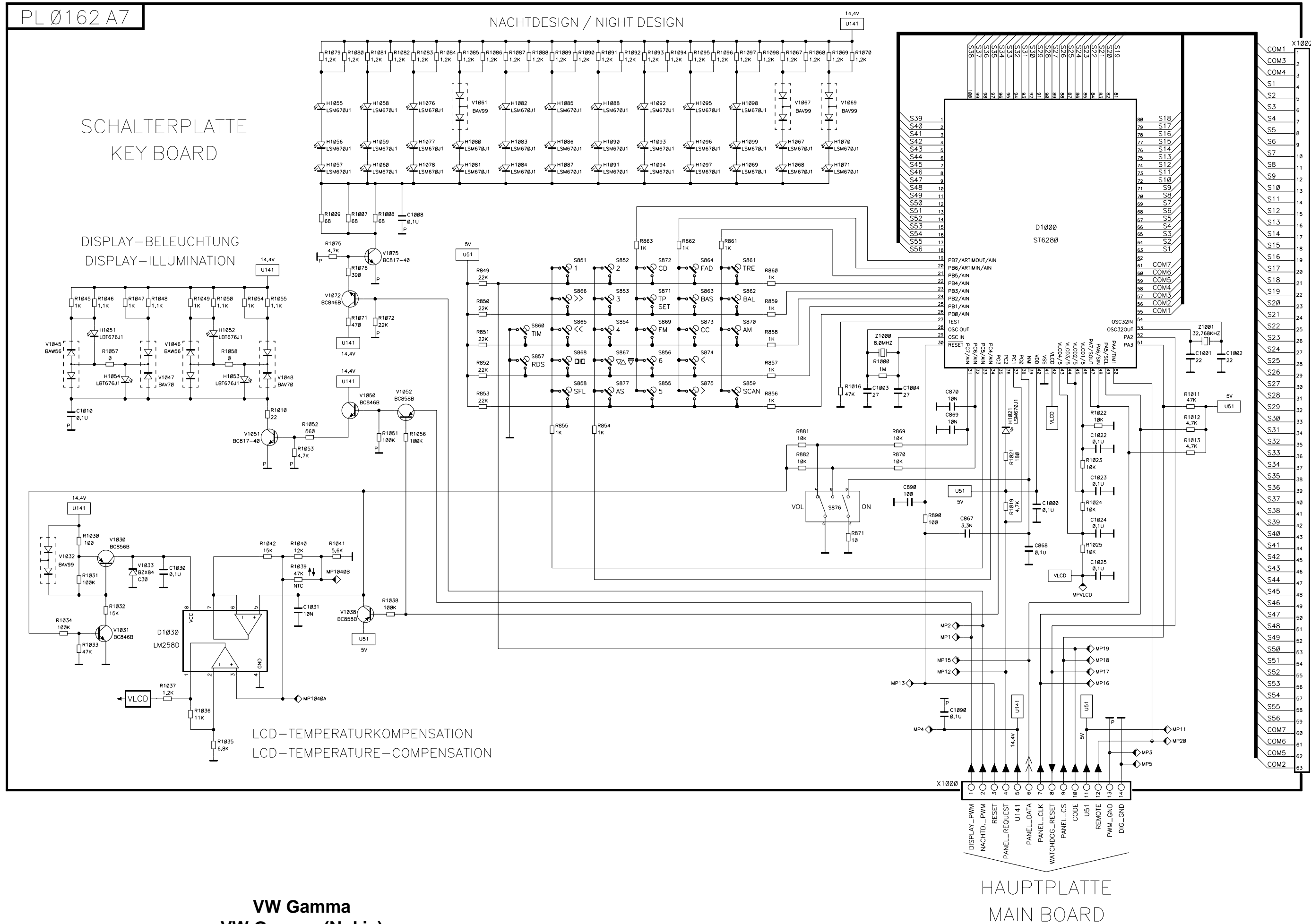


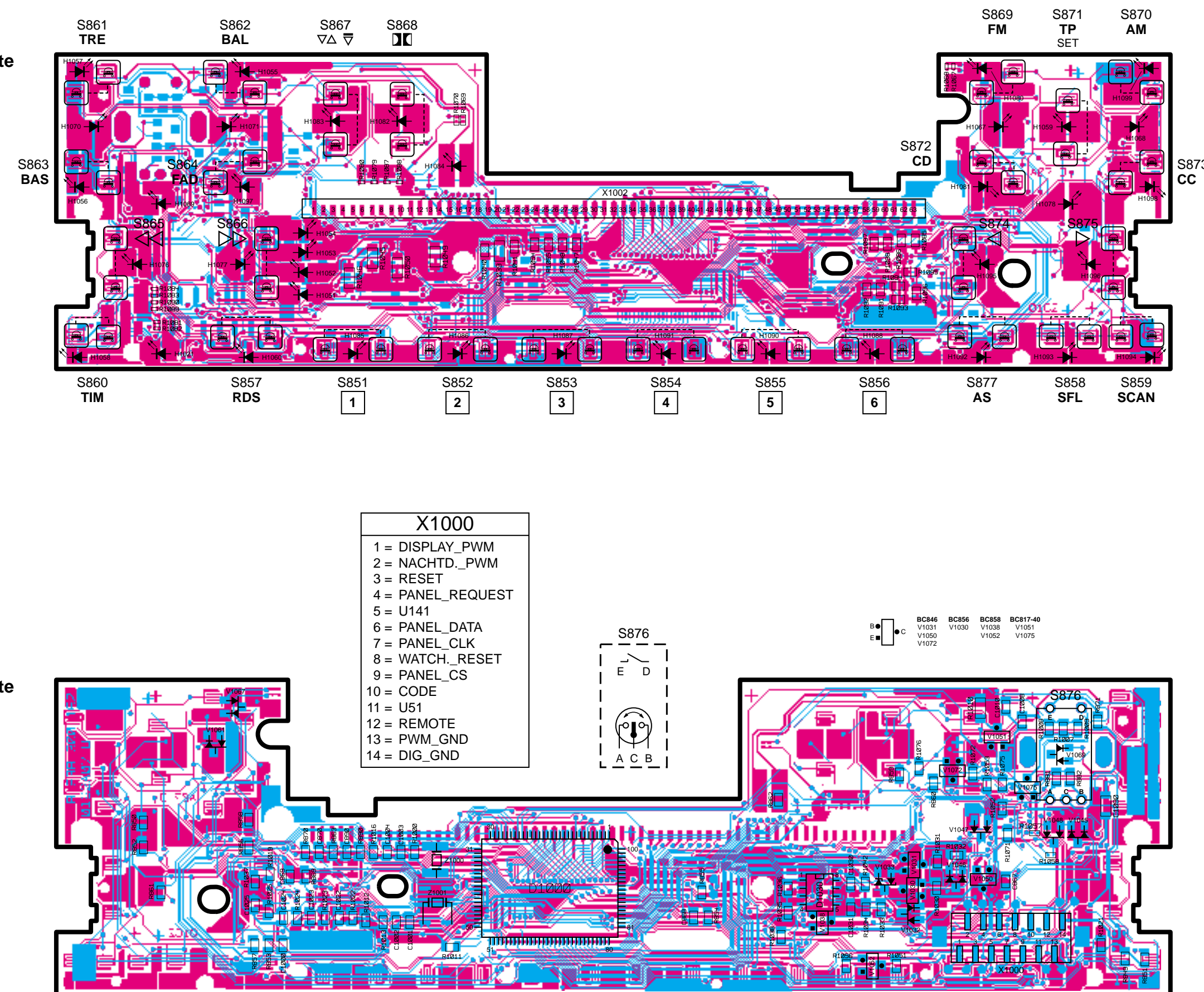
Pin-Belegung des IC D150				
Digital IC D150 Pin Configuration				
Pin No.	I/O	Name	Funktion	Function
1	-	VSS	Masse	Ground
2	-	VDD	5 V	5 V
4	I	TDI	Testdateneingang	Test Data Input
5	I	TMS	Test Mode	Test Mode
6	I	TRC	Test Clock	Test Clock
7	-	VDD	5 V	5 V
8	-	VSS	Masse	Ground
9	-	PDOWN	Power down Zustand	Power down Mode
10	I	RESET	Hardware reset (active LOW)	Reset
11	O	IRQN	RDS Alarm/SLSS	RDS alarm/search stop
12	I	CSN	Chip select Eingang	Chip select μ C interface
13	I	SCSPKX	Serial Data μ C Interface	Serial data μ C interface IN
14	O	SCPTX	Serial Data μ C Interface	Serial data μ C interface OUT
15	I	VDIOL	Clock μ C Interface	Clock μ C interface
16	O	VDIOL	Pushpulling Digital En-/Ausgänge	Pushpulling Digital En-/Ausgänge
17	-	VSSIO	Masse Digitale Ein-/Ausgänge	Ground for digital I/O
18	O	CLK1	Programmable clock 1	Programmable clock 1
20	I	XTAL1	28.5 MHz Oszillator	Oscillator 28.5 MHz
21	O	XTAL0	28.5 MHz Oszillator	Oscillator 28.5 MHz
32	-	VDD	5 V	5 V
33	-	VSS	Masse	Ground
35	-	VSSPLL	Masse (minu) PLL	PLL 5V (pos.)
36	-	VDDPLL	Plus PLL 5V	PLL 5V (pos.)
37	O	REFP1	Audio D/A-Wandler Positive Referenz	Audio D/A converter (pos. reference)
38	O	REFN1	Audio D/A-Wandler Negative Referenz	Audio D/A converter (neg. reference)
39	-	CAPN	PLL Kapazität (negativ)	PLL capacity (neg.)
40	-	CAPP	PLL Kapazität (positiv)	PLL capacity (pos.)
41	-	VDDIO	Audio D/A-Wandler 5V	Audio D/A converter (+5V)
42	-	VSSIO	Audio D/A-Wandler Masse	Audio D/A converter (ground)
44	O	RFO	Audio Rectifier (analog)	Analogic audio right
45	-	VSSA	Masse Analogic Eingänge	Ground
46	-	LFO	Audio Links (analog)	Analogic audio left
48	-	VDDA	5V A/D-Wandler	5V A/D-converter
49	-	VSSA	Masse A/D-Wandler	Ground A/D-converter
50	O	REFP3	Audio D/A-Wandler Positive Referenz	Audio D/A converter (pos. reference)
51	O	REFN3	Audio D/A-Wandler Negative Referenz	Audio D/A converter (neg. reference)
52	I	AUXL	Extener Eingangs links	Auxiliary left
53	I	AUXR	Extener Eingangs rechts	Auxiliary right
54	-	AGND	Audoeingänge Masse	Ground for Audio inputs
55	I	CCR	Cassette Eingangs rechts	Cassette input right
56	I	AUXR	Extener Eingangs rechts	Auxiliary right
57	-	VDDR	5 V	5 V
58	-	VSSR	Masse	Ground
59	O	REFP2	Audio D/A-Wandler Positive Referenz	IF A/D converter (pos. reference)
60	I	IFP	ZF Eingangs (positiv)	IF A/D converter (pos.)
61	I	IFN	ZF Eingangs (minu)	IF input (neg.)
62	O	REFN2	Audio D/A-Wandler Negative Referenz	Audio D/A converter (neg. reference)
64	-	VDDIF	ZF A/D-Wandler (minu)	ZF A/D-converter (neg.)
65	-	VDDIF	ZF A/D-Wandler 5 V	IF A/D converter (+5V)



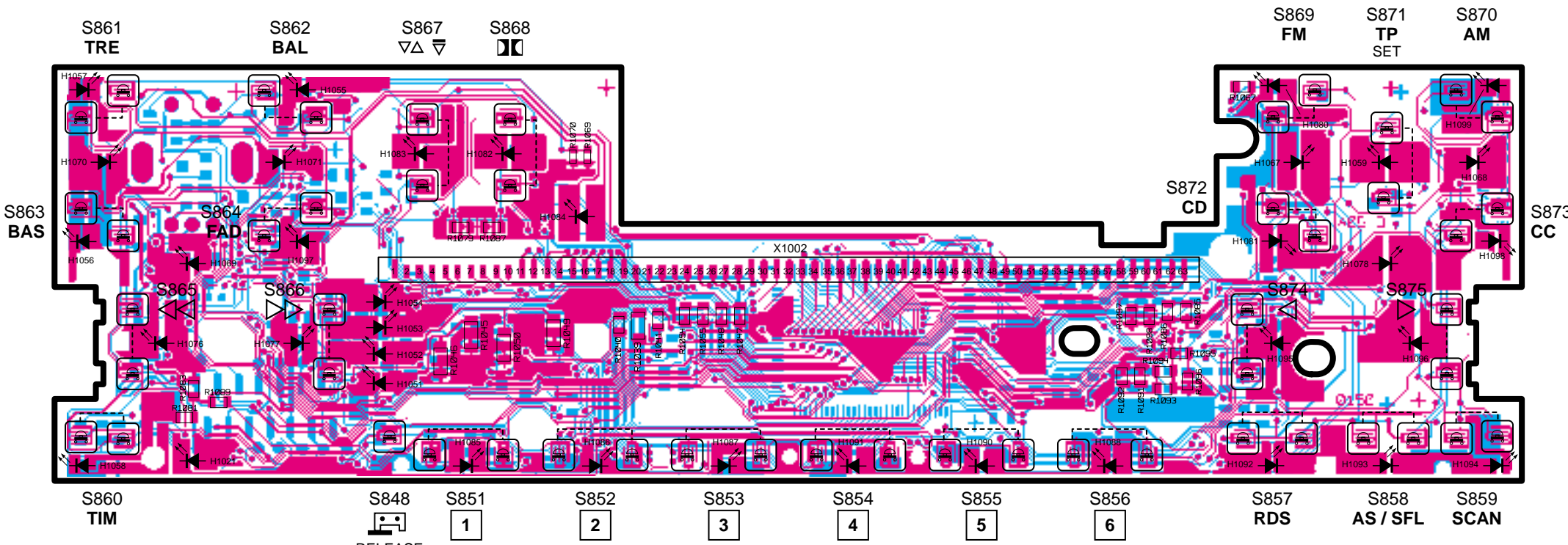


Schalterplatte
Key board
PL 0162 A07
Chip

Schalterplatte
Key board
PL 0162 A07
+ Chip

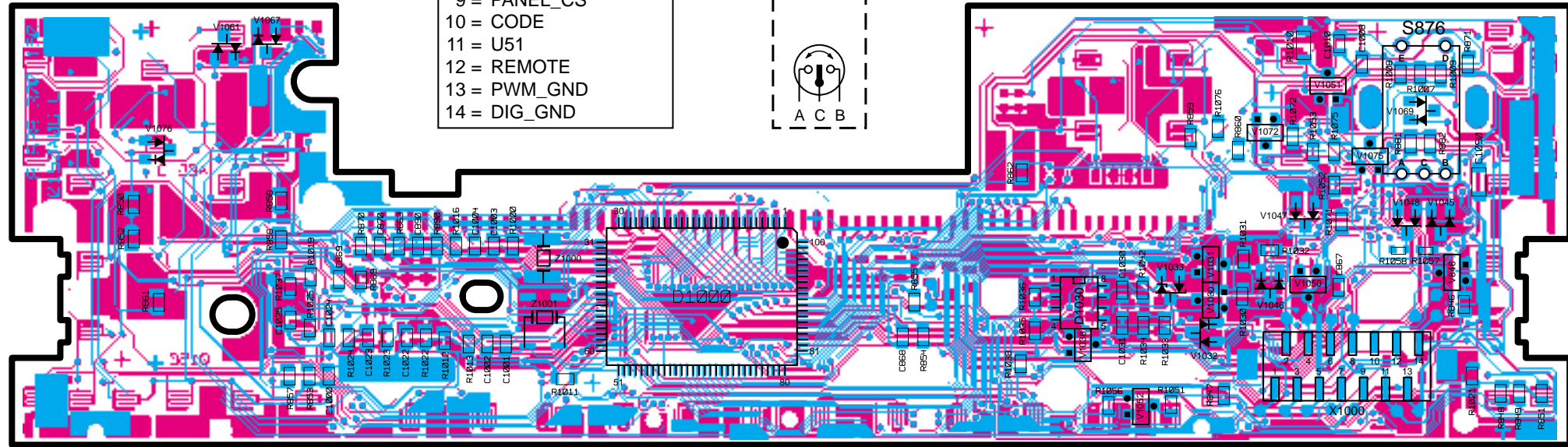


Schalterplatte
Key board
PL 0150 A06
Chip



- X1000
- 1 = DISPLAY_PWM
 - 2 = NACHTD._PWM
 - 3 = RESET
 - 4 = PANEL_REQUEST
 - 5 = U141
 - 6 = PANEL_DATA
 - 7 = PANEL_CLK
 - 8 = WATCH._RESET
 - 9 = PANEL_CS
 - 10 = CODE
 - 11 = U51
 - 12 = REMOTE
 - 13 = PWM_GND
 - 14 = DIG_GND

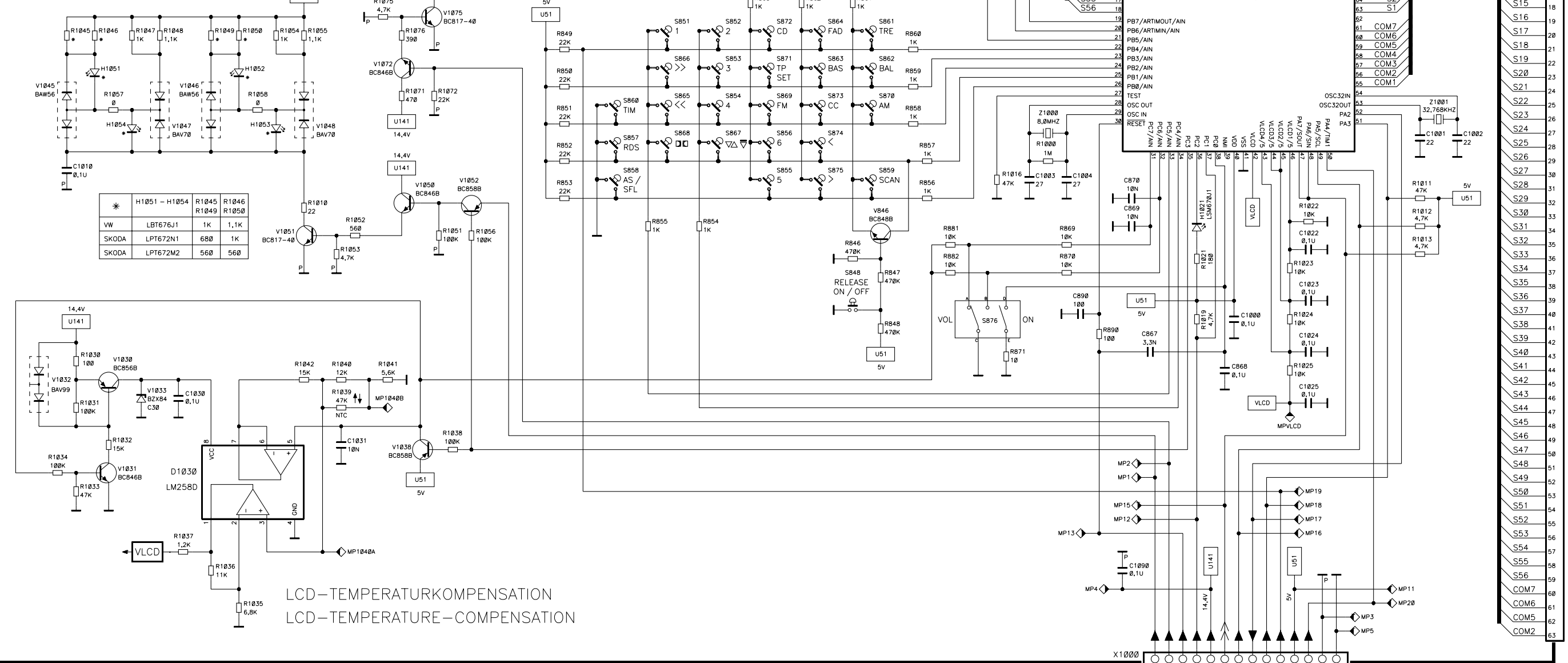
Schalterplatte
Key board
PL 0150 A06
+ Chip



PL 0150 A6

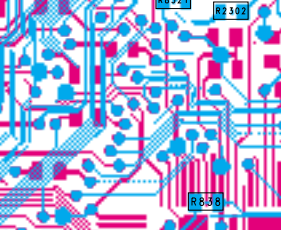
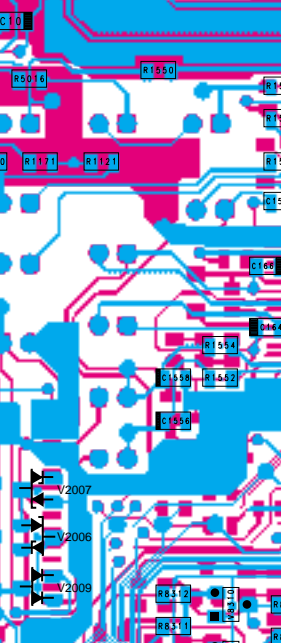
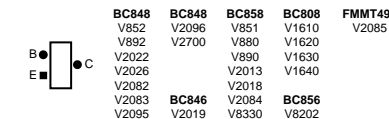
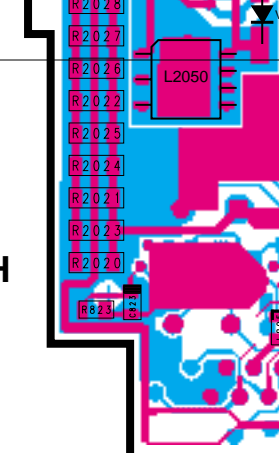
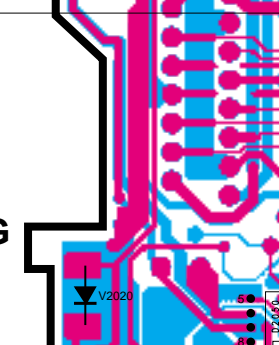
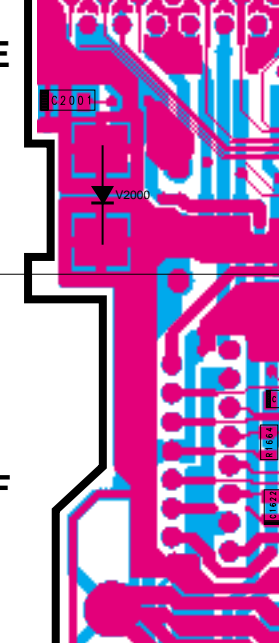
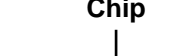
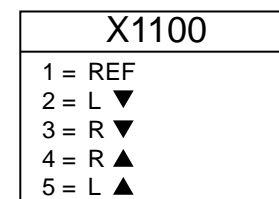
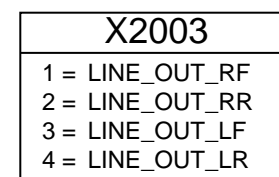
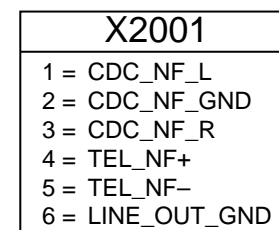
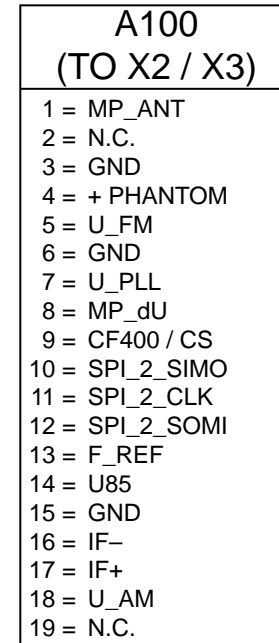
SCHALTERPLATTE
KEY BOARD

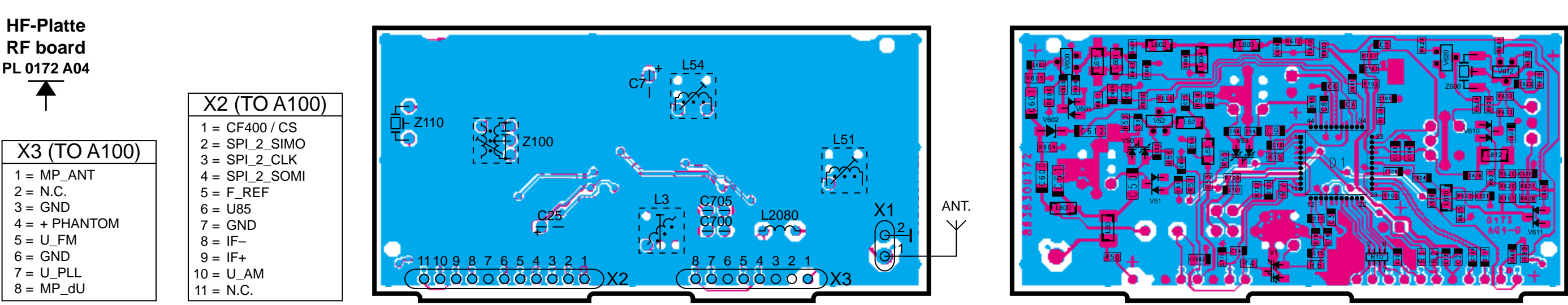
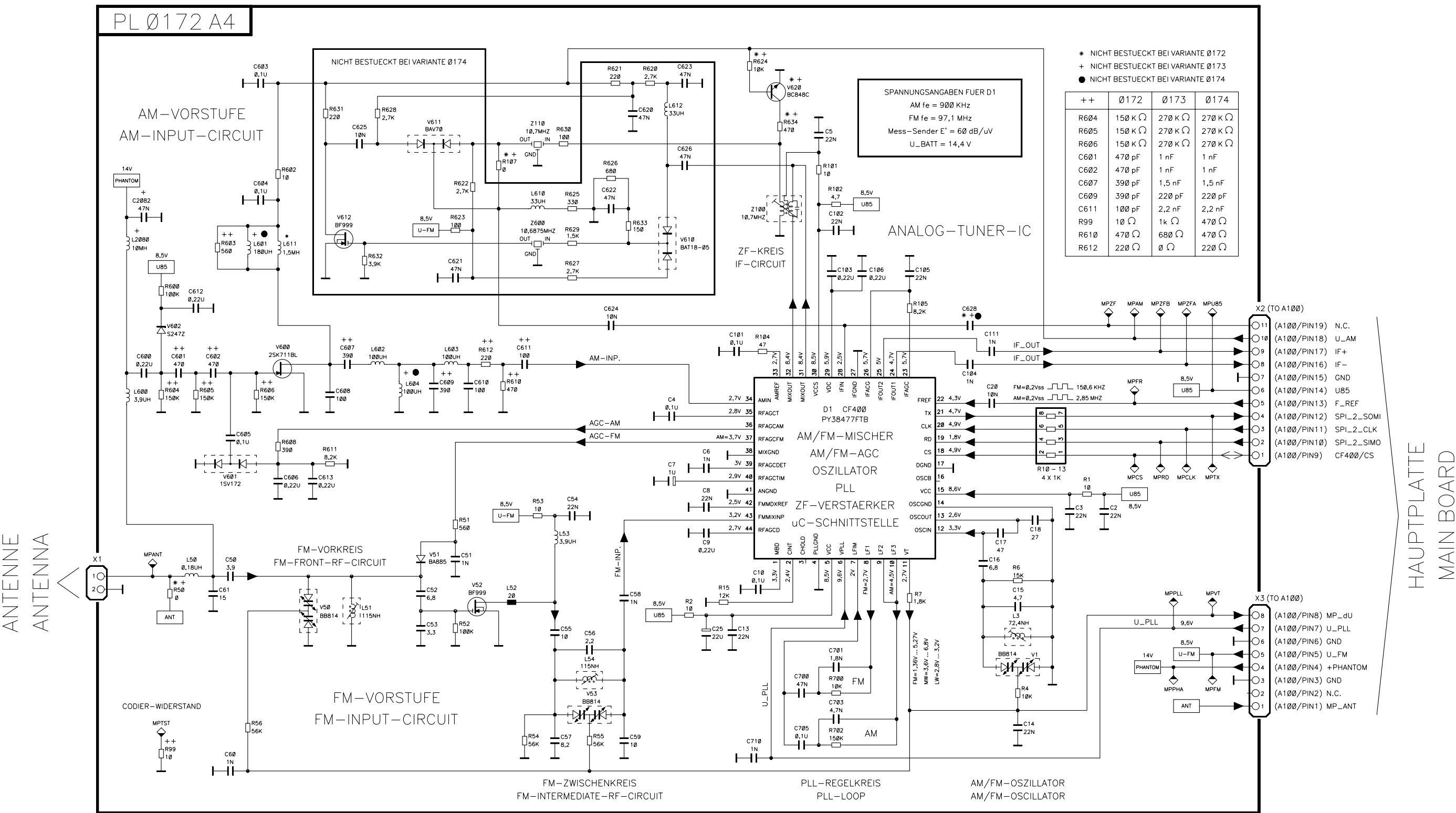
DISPLAY-BELEUCHTUNG
DISPLAY-ILLUMINATION



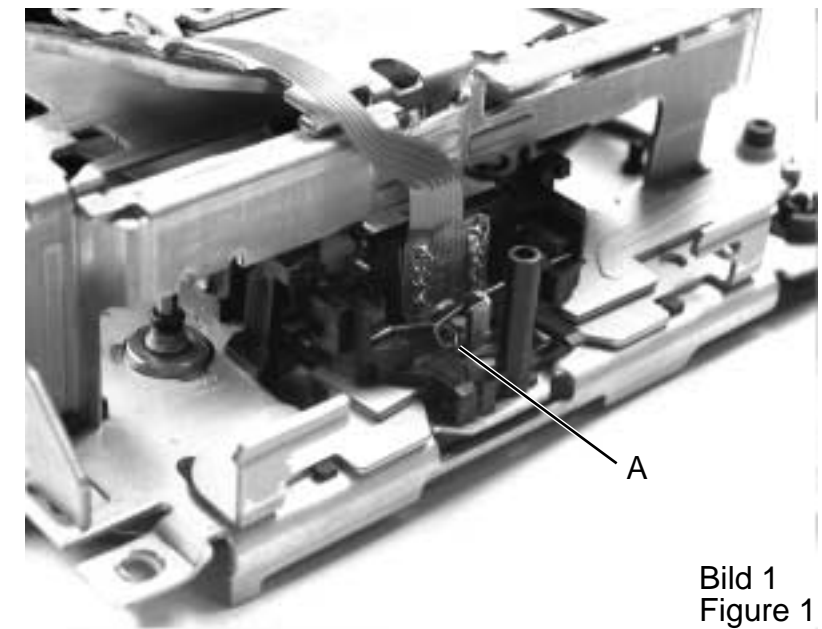
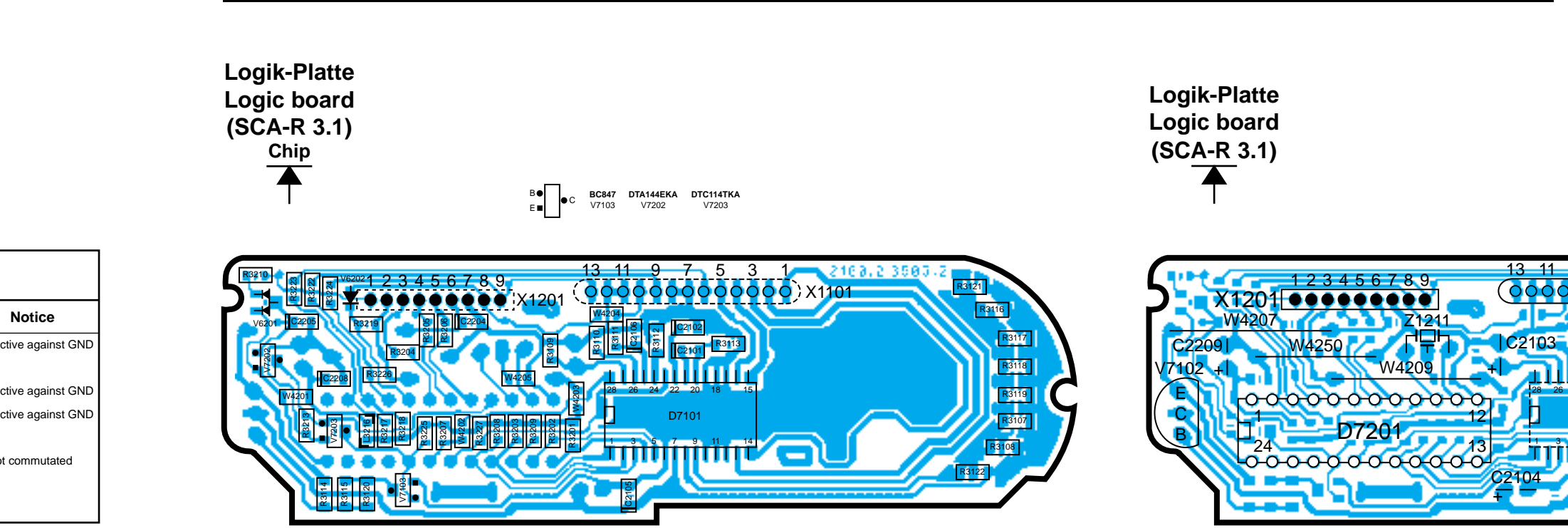
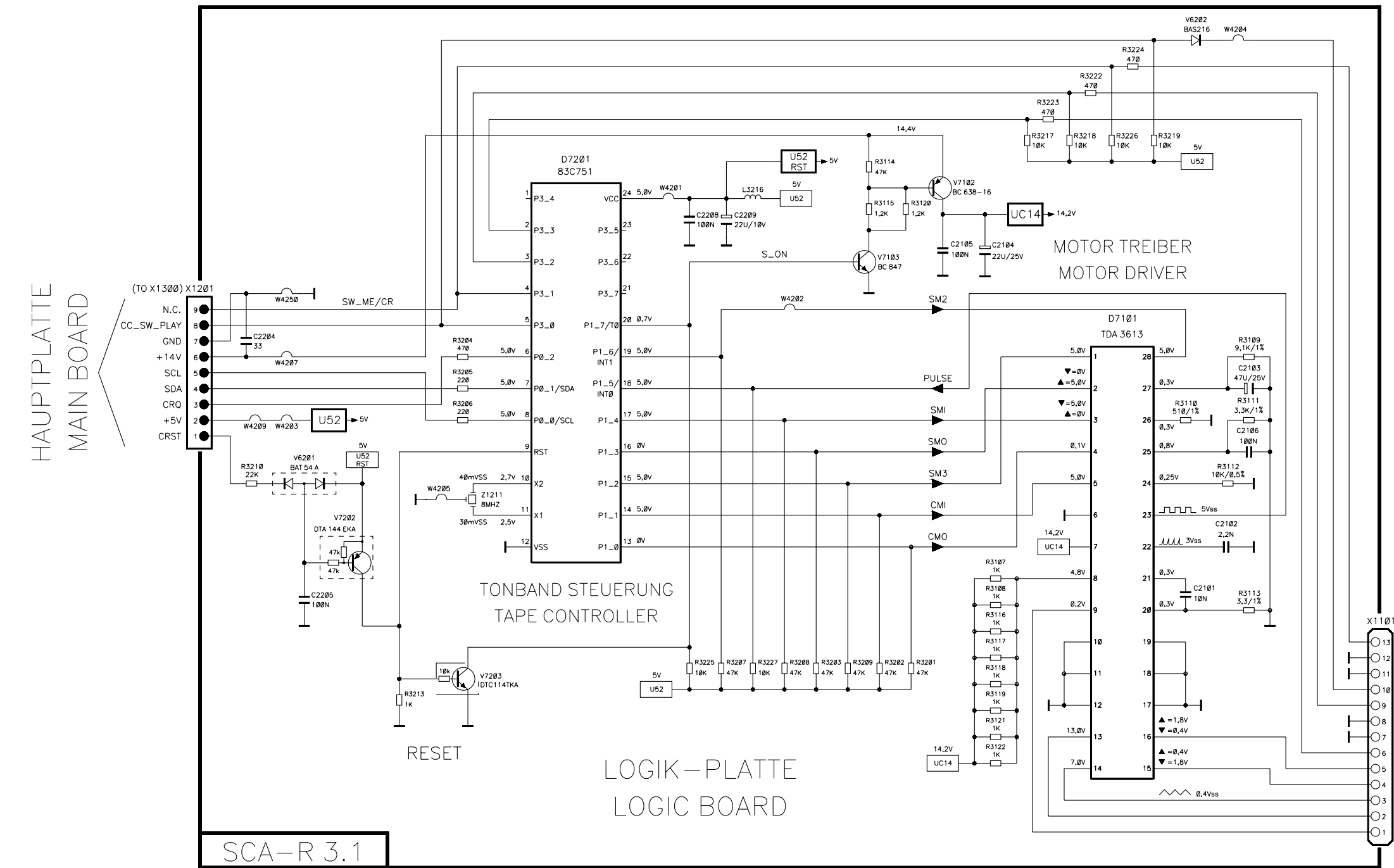
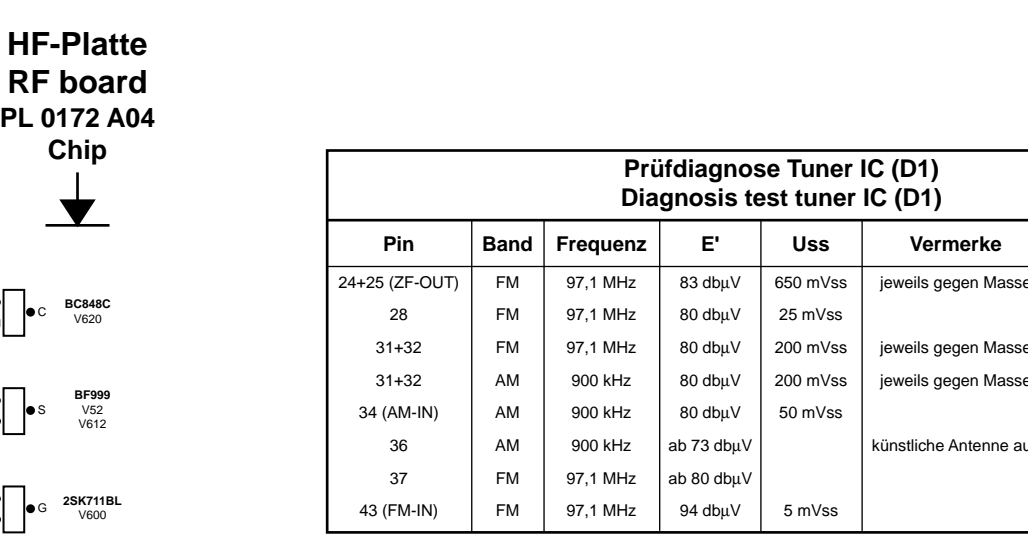
VW Gamma Release
VW Gamma Release (Nokia)
Skoda Gamma Release

HAUPTPLATTE
MAIN BOARD





Pin-Belegung des FM/AM Tuner-IC D1 Tuner IC D1 Pin configuration				
Pin No.	I/O	Name	Funktion	Function
1	-	MIXDEC	Mischer Entkopplung für PLL	Mixer decoupling for PLL
2	-	CINT	CHOLD	for PLL
3	-			
4	-	PLL_GND	PLL - Masse	PLL Ground
5	-	VCC	8,5V	8,5V
6	-	VPLL	PLL Oberspannung	PLL top voltage
7	I	LF1N	Schleifenfiltereingang	PLL loop filter Input
8	O	LF1	Schleifenfilter 1	PLL loop filter Output 1
9	O	LF2	Schleifenfilter 2	PLL loop filter Output 2
10	O	LF3	Schleifenfilter 3	PLL loop filter Output 3
11	I	VTUNE	Abstimmungsspannung	Tuning voltage
12	I	OSCINP	Oszillator Eingang	Oscillator Input
13	O	OSCOU	Oszillator Ausgang	Oscillator Output
14	-	OSCGND	Oszillator Masse	Oscillator Ground
15	O	VCC	8,5V	8,5V
16	O	OSCBUF	Oszillatorausgangstreiber	Oscillator Buffer Output
17	I	DGND	Digitale Masse	Digital Ground
18	I	CS	Chip Select	Chip Select
19	I	RD	Dateneingang	DATA IN
20	I	CLK	Clock	Clock
21	O	TX	Datenausgang	DATA OUT
22	I	FREF	Referenzfrequenz	Reference frequency
23	-	IFAGC2	IF Regelspannung 2	IF AGC 2
24	O	IFOUT1	ZF - Ausgang 1	IF output 1
25	O	IFOUT2	ZF - Ausgang 2	IF output 2
26	-	IFAGC1	ZF Regelspannung 1	IF AGC 1
27	-	IFGND	ZF Masse	IF Ground
28	I	IFIN	ZF Eingang	IF Input
29	-	VDC	Interne Referenzspannung	Internal reference voltage
30	-	VCC	8,5V	8,5V
31	O	MIXOUT2	Mischerausgang 2	Mixer Output 2
32	O	MIXOUT1	Mischerausgang 1	Mixer Output 1
33	-	AMREF	AM - Referenzeingang	AM reference input
34	I	AMMIXIN	AM Mischereingang	AM Mixer Input
35	-	RFAGC3	HF Regelzeitkonstante (aufregeln)	RF AGC 3
36	O	RFAGCAM	HF Steuerspannung Vorstufe AM	RF AGC for AM input stage
37	O	RFAGCFM	HF Steuerspannung Vorstufe FM	RF AGC for FM input stage
38	-	MIXGND	Mischer Masse	Mixer Ground
39	-	RFAGC2	HF Regelzeitkonstante (Detektor)	RF AGC 2
40	-	RFAGC1	HF Regelzeitkonstante (abregeln)	RF AGC 1
41	-	ANGND	Analog Masse	Analog ground
42	-	FMMIXREF	Referenzspannung FM Mischer	Reference voltage FM mixer
43	I	FMMIXINP	FM Mischer Eingang	FM mixer input
44	-	RFAGCD	AGC Entkopplung	AGC decoupling



Bei Tonkopfwechsel installieren Sie bitte die Feder (A) wie im Bild 1.

With a replacement of the tape audio-head please place the spring (A) as is shown in figure 1.

X1101

1 = CAPSTAN_MOTOR -	8 = SW_PLAY_GND
2 = CAPSTAN_MOTOR +	9 = SW_PLAY
3 = CAPSTAN_MOTOR	10 = SW_INSERT
4 = LIFT_MOTOR -	11 = SW_INSERT_GND
5 = LIFT_MOTOR +	12 = SW_ME / CR_GND
6 = SW_STANDBY	13 = SW_ME / CR
7 = SW_STANDBY_GND	

X1201
(TO X1300)

1 = CRST
2 = +5V
3 = CRQ
4 = SDA
5 = SCL
6 = +14V
7 = GND
8 = CC_SW_PLAY
9 = N.C.